

RTES Forum Agenda
Arabella Sheraton Grand Hotel
Munich, Germany
20-23 October 2008

RTES Forum Overview Agenda

Munich, Germany

20-23 October, 2008

- Monday, 20 October, 1400-1800 -- Architecture Analysis and Design Language (*AADL*) -- Programs, Tools and Formal Methods
- Tuesday 21 October, 0900-1700 -- Dependability through Assuredness
- Wednesday 22 October, 0900-1530 -- Dependability through Assuredness
- Wednesday 22 October, 1600-1830 -- Architecting to the Edge Working Group via Conference Bridge
- Thursday 23 October, 0900-1500 -- MILS Architecture
- Thursday 23 October, 1500-1830 -- MILS Architecture Working Group via Conference Bridge
- Monday-Thursday 20-23 October-- JSR 302 SC Java Expert Group - A parallel session.

Monday, 20 October 1430-1800

Architecture Analysis and Design Language (AADL) Programs, Tools and Formal Methods

- 1400-1430 RTES Forum Introduction – Joe Bergmann, The Open Group
- 1430-1500 Overview of AADL Version 2 New Capabilities -- Peter Feiler, SEI
- 1500-1520 TOPCASED and SPICES Update -- Vincent Seignole, Thales, Pierre Gaufillet, Airbus
- 1520-1540 Interfacing from AADL Specifications to Model Checkers -- Mamoun Filali, IRIT
- 1540-1600 Using Alloy for Model Checking AADL Architecture Specifications,
Analyzing a Redundant Architecture with Chaotic Models --Dionisio de Niz, SEI
- 1600-1620 Break
- 1620-1720 ASSERT Program Review and Future Plans for AADL (1 hour), --
Eric Conquet, ASSERT Program Manager, ESA
- 1720-1740 Demonstrating the ASSERT tool chain, -- Jerome Hugues, ENST
- 1740-1800 - Extending OCARINA Integration and Generation with Simulink, --
Jerome Hugues, ENST

Tuesday, 21 October 0900-1700

“Dependability through Assuredness”

- 0900-1000 Dr Manfred Broy, Architecture Based Specification and Verification of Embedded Software Systems, Technical University Munich
- 1000-1030 Jack Fujieda Re-Gis
- 1030-1115 Yuusuke Hashimoto, NEC
- 1115-1230 Dr Shuichiro Yamamoto, NTT Data
- 1230-1400 Lunch
- 1400-1445 Kazuaki Minami, Toyota
- 1445-1515 Rance DeLong, Compositional Certification – AFRL/Raytheon
- 1515-1545 Break
- 1545-1630 Julia Allen, Business-Based Assurance Investment Decisions - CMU SEI
- 1630-1700 Joe Bergmann, A Vision - The Open Group

Wednesday, 22 October 0900-1830

"Dependability through Assuredness" (Cont'd)

- 0900-0945 Dr Wolfram, "Fully Formal" Functional Verification of Digital Hardware Components – Founder of Abstract RT Solutions
- 0945-1030 Dr Angelo Corsaro, Development Environments for the *Multicore* Architecture - PrismTech
- 1030-1100 Break
- 1100-1130 Rance DeLong – LynuxWorks, San Diego Research Day
- 1130-1200 Sam Ceccola, Scalability, Simplicity, Agility, Visibility and Dependability for Enterprise Java Infrastructure – Azul Systems
- 1200-1230 TBC - BMW, Industry Verification Experience (invited)
- 1230-1400 Lunch
- 1400-1445 Dr. Mirko Jakovljevic, Achieving the Dependability Requirement -- TTTech
- 1445-1530 Jeremy Epstein – What Measures Should be Used for Assurance? -- Principal Consultant, Cigital
- 1530-1600 Break
- 1600-1830 – "Architecting to the Edge" Working Group meeting via the conference bridge.

Thursday, 23 October 0900-1800

MILS Architecture

- 0900-1000 Ben Calloni, Why MILS? - Lockheed-Martin
- 1000-1030 Boyer-Vidal, Partitioning Communication System -Objective Interface Systems
- 1030-1100 Break
- 1100-1230 Rob Hoffman, Total Solution - Wind River
- 1230-1330 Lunch
- 1330-1415 Rance DeLong, Common Criteria, EAL 8 - AFRL/Raytheon
- 1415-1500 Knut Degen, SYSGO (invited)
- 1500-1530 Break
- 1530-1800 MILS Architecture Working Group members meeting via the conference bridge.